



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 09/916,619  
Applicant : Chih Hsin Wang  
Filed : July 26, 2001  
TC/A.U. : 2818  
Examiner : Renee R. Berry  
Title : A SEMICONDUCTOR MEMORY ARRAY OF FLOATING  
GATE MEMORY CELLS WITH CONTROL GATE SPACER  
PORTIONS (as amended herein)

Docket No. : 2102397-910600  
Customer No. : 26379

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on December 2, 2003.

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

GRAY CARY WARE & FREIDENRICH Date: 12/2/03

By: Kathleen LaBrie

Kathleen LaBrie

**RESPONSE TO OFFICE ACTION OF AUGUST 7, 2003**

Sir:

In response to the Office Action of August 7, 2003, please amend the above identified application as follows:

**Amendments to the Specification** begin on page 2 of this paper.

**A listing of the Pending Claims** begins on page 3 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.

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